NEWS RELEASE

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AppliedMicro Chooses Infinisim RASER™ for Verification of Its Advanced SoCs and Mixed-signal Designs

Leader in Telco and embedded ICs selects advanced process node verification tool to simulate large number of process corners

SAN JOSE, Calif. – Sept. 15, 2009 – Infinisim, a provider of innovative verification solutions for mixed-signal designs, announced today that AppliedMicro (NASDAQ: AMCC), a global leader in energy conscious computing and communications solutions, has selected its flagship verification product, RASER™, as the verification platform for next-generation designs.

"AppliedMicro has transitioned to sub-nanometer advanced process technologies where statistical effects are significant, therefore requiring verification of a large number of process corners," said Mr. Babak Mansoorian, Corporate IC Manager, AppliedMicro. "Well proximity effects, low voltage swings, high device density and higher clock frequencies all demand highly accurate spice simulations so that the correct circuit behavior can be ensured. These effects also require the simulations to be run at full-chip capacity. This increasing verification complexity is not addressed fully by the existing mixed-signal simulators. With Infinisim RASER, we were able to run concurrent simulations for very large test cases at multiple corners thereby increasing the overall verification coverage in a limited time. We have incorporated it into our simulation and verification flow for advanced process technologies."

Infinisim's RASER technology bridges the gap between advanced process technology verification requirements and capabilities of existing mixed signal simulators. RASER, with its innovative Real-time Adaptive Simulation™, guarantees SPICE accurate results with an average of 50 times higher throughput and capacity, enabling AppliedMicro to verify large mixed-mode SoCs and mixed-signal designs. RASER is production proven and currently in use at many mixed-signal design houses for their verification.

"We are happy that AppliedMicro selected RASER as the spice-level verification simulator for their most advanced designs. Verification complexity is growing dramatically with a big increase in the required number of simulations and the accuracy required. RASER is built on a platform

to address these challenges natively. AppliedMicro's adoption of RASER is further validation that RASER is the highest-throughput SPICE-accurate verification solution in the market, thus uniquely positioning it to address the growing verification complexity in advanced process nodes," said Ms. Samia Rashid, President of Infinisim. "We look forward to aggressively maintaining our position as leader in the role of solving our customers' most challenging leading-edge technology problems".

About Infinisim

Infinisim provides guaranteed SPICE-accurate simulation results with an average of 50 times higher throughput and capacity for large mixed-signal circuits. Patent-pending Real-time Adaptive Simulation™ (RAS™) technology makes the simulator always accurate and always fast. Infinisim's RASER™ is highly effective for analog and large scale analog mixed-signal circuits. RASER's speed and capacity uniquely position it to run simulations at all stages of design verification - from single-block to full-chip, from pre-layout to post-layout. Infinisim RASER was named one of the Hot 100 electronic products of 2008 by EDN Magazine. Infinisim enables its customers to eliminate silicon respins, reduce chip design schedules and dramatically improve product quality and production yield. Infinisim is privately held and based in Santa Clara, Calif. For more information, please visit www.infinisim.com.

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